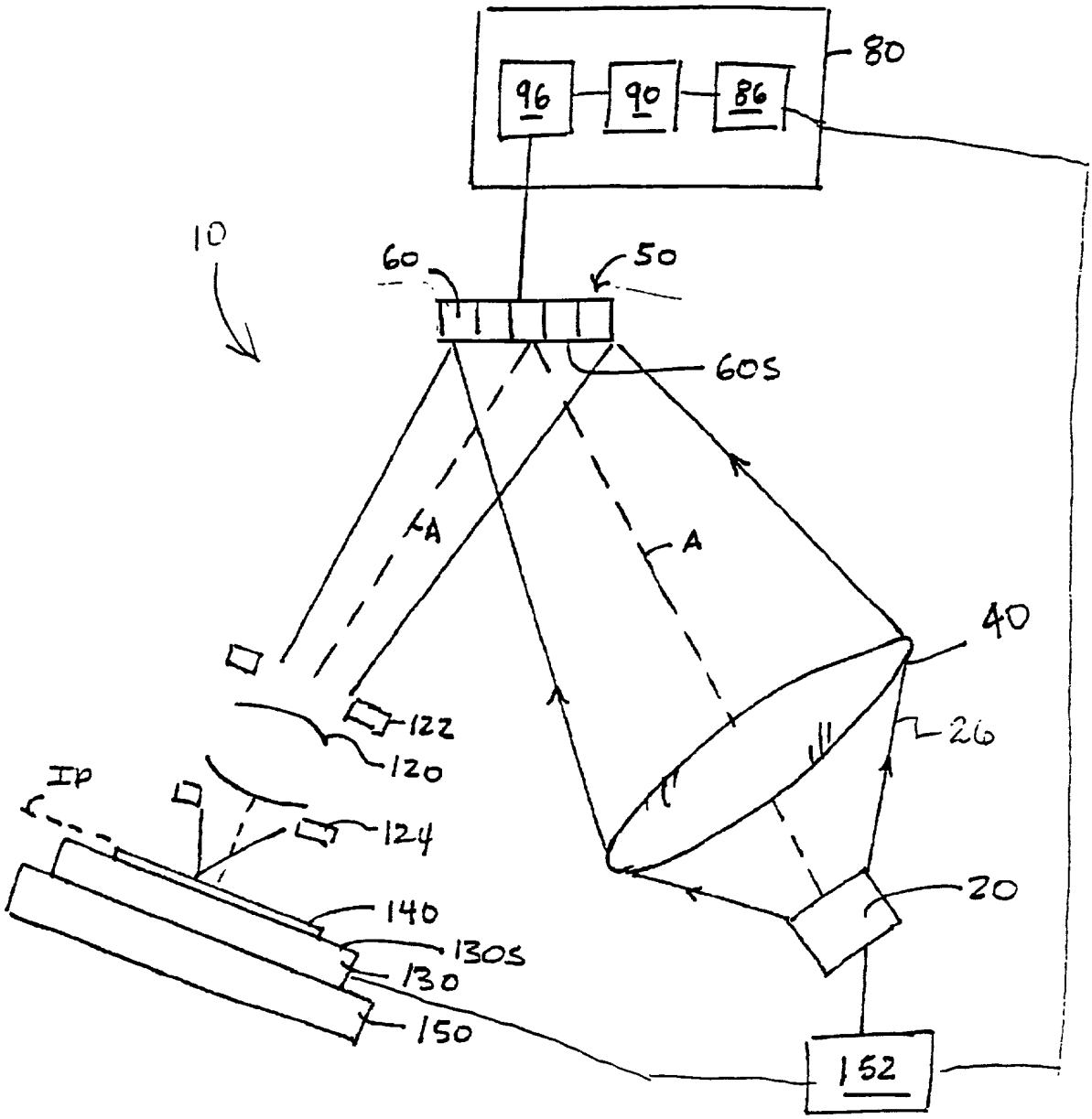
80
96
90
86
10
60
50
60S
A
A
40
122
120
26
IP
124
20
140
130S
130
150
152

FIG. 1

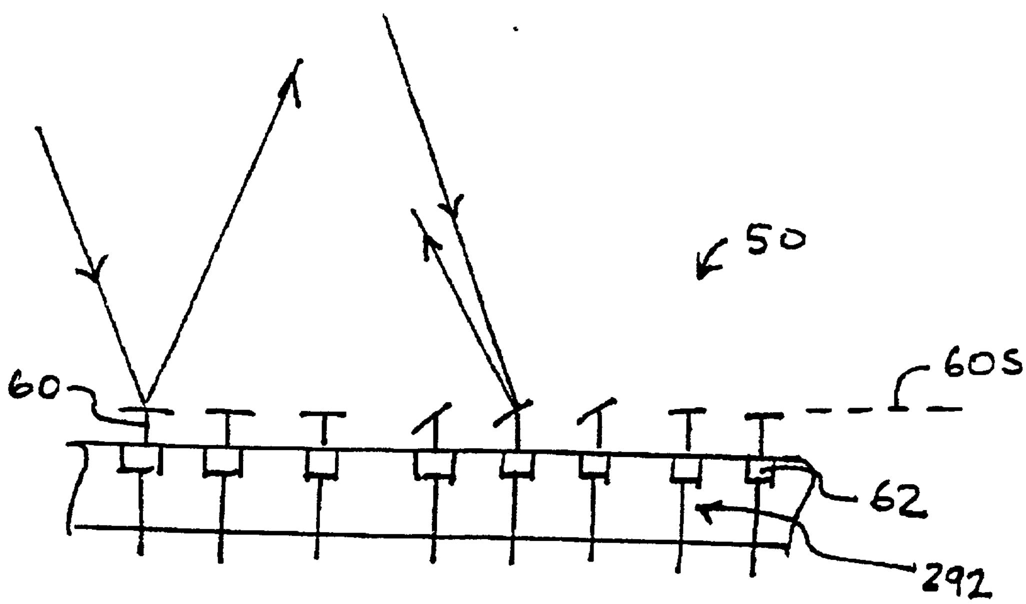

FIG. 2

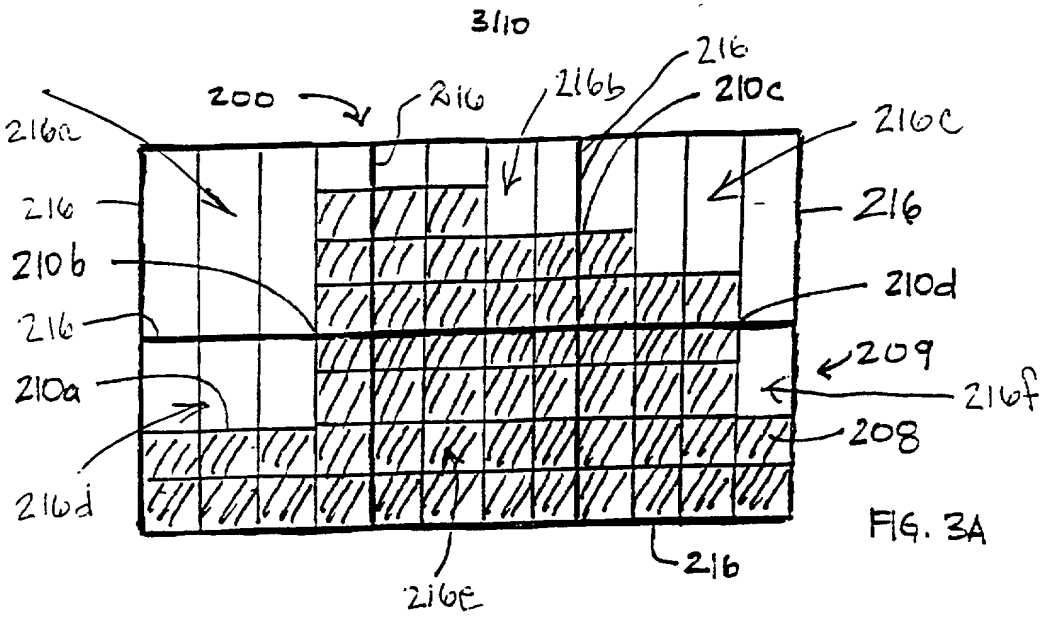

FIG. 3A

216a	216b	216c
$3/16 \times 31 = 6$ (2+4)	$\frac{10}{16} \times 31 = 19$ (1+2+16)	$\frac{4}{16} \times 31 = 8$ (8)
216d	**216e**	**216f**
$\frac{10}{16} \times 31 = 19$ (1+2+16)	$\frac{16}{16} \times 31 = 31$ (1+2+4+8+16)	$\frac{14}{16} \times 31 = 27$ (1+2+8+16)

FIG. 3B

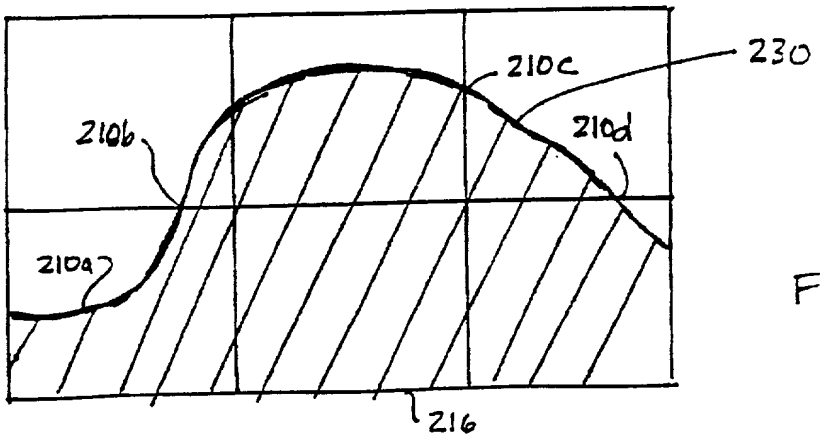

FIG. 3D

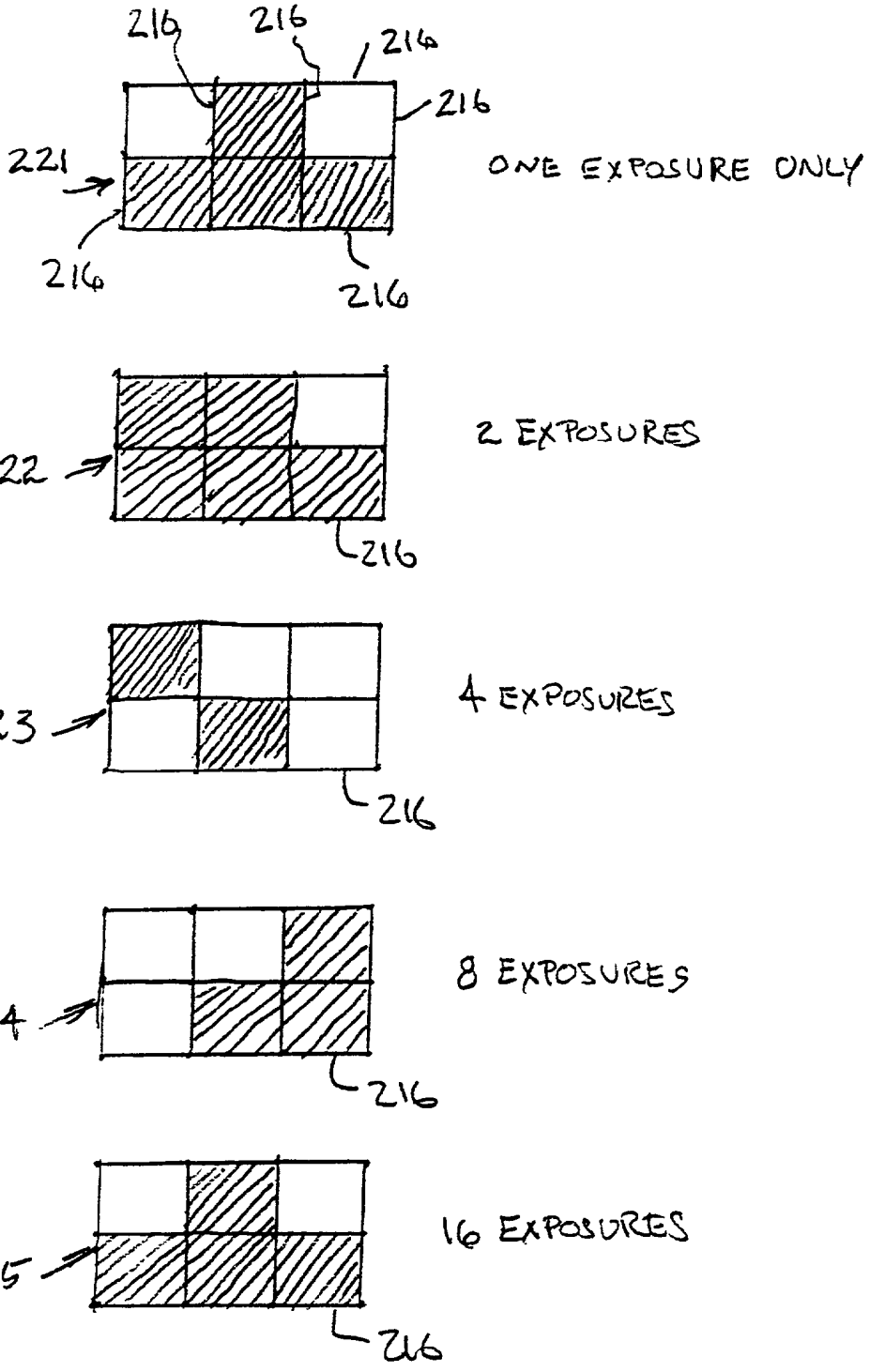

FIG. 3C

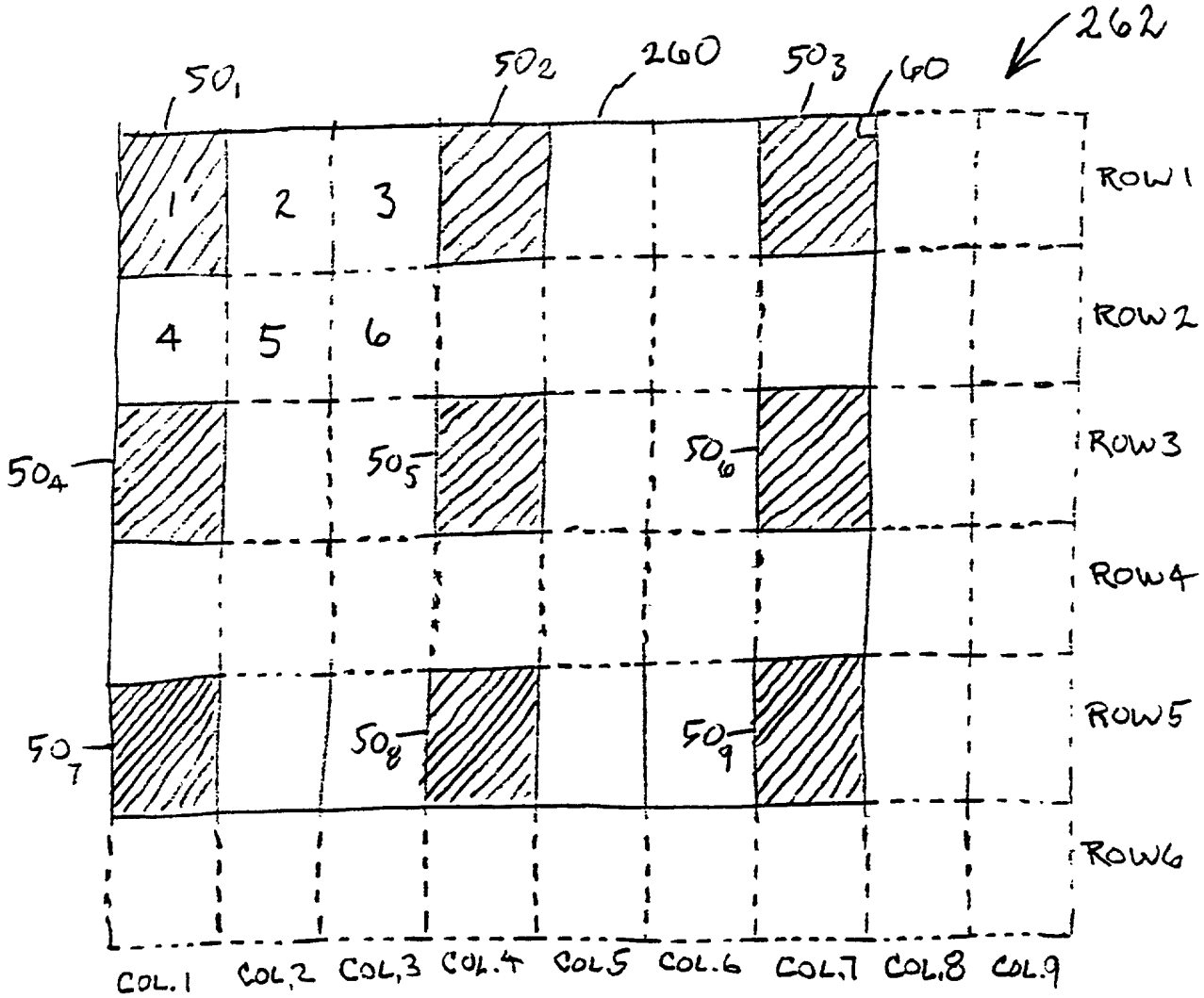
262
50_1
50_2
260
50_3
60
1
2
3
4
5
6
ROW1
ROW2
ROW3
ROW4
ROW5
ROW6
50_4
50_5
50_6
50_7
50_8
50_9
COL.1
COL.2
COL.3
COL.4
COL.5
COL.6
COL.7
COL.8
COL.9

FIG. 4

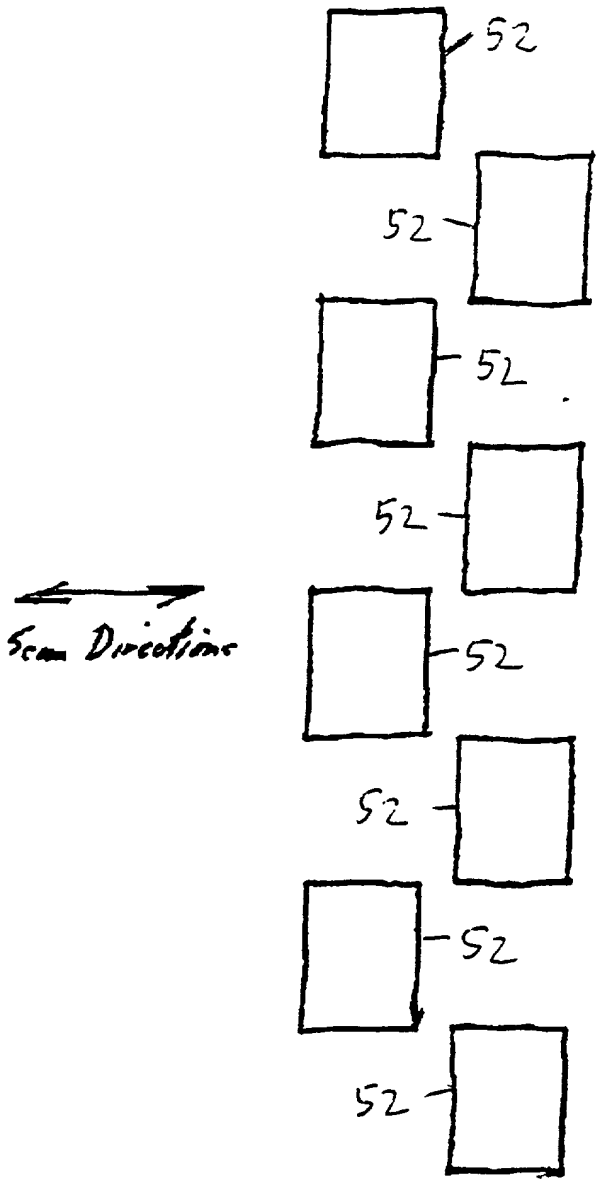

FIG 5

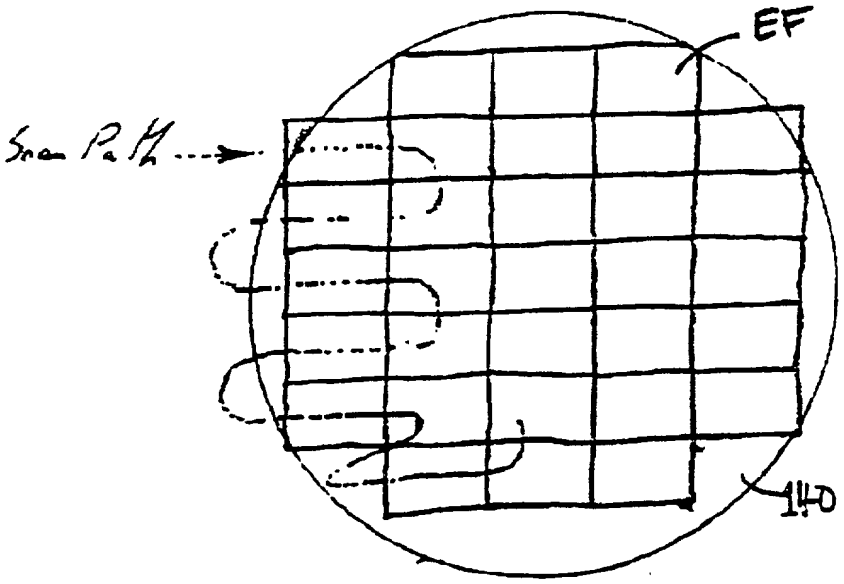

FIG. 6A

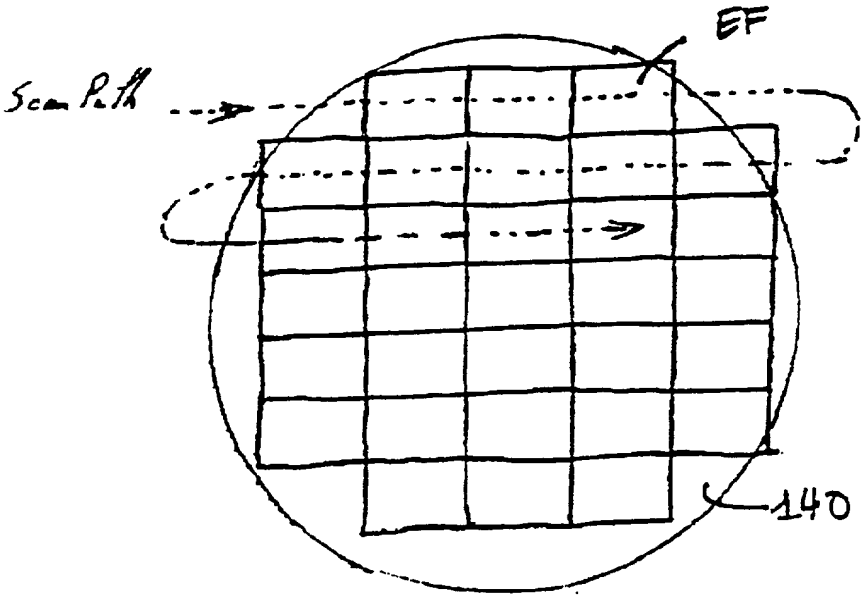

FIG. 6B

Figure 6

Pattern Manipulation Example for a Scan and Flash System with n = 4

Memory #1

Anothe r adva tage o f usin g mult iple e xposur es of a micr o-mirr or arr ay is that I t affo rds th e poss ibilit

Memory #2

Anothe r adva tage o f usin g mult iple e xposur es of a micr o-mirr or arr ay is that I t affo rds th e poss ibilit

Memory #3

Anothe r adva tage o f usin g mult iple e xposur es of a micr o-mirr or arr ay is that I t affo rds th e poss ibilit

Memory #4

Anothe r adva tage o f usin g mult iple e xposur es of a micr o-mirr or arr ay is that I t affo rds th e poss ibilit

Image Transducer Paterns at Times t-1 through t-16

t-1															**Anothe**
t-2														**Anothe**	***r adva***
t-3													**Anothe**	***r adva***	***tage o***
t-4												**Anothe**	***r adva***	***tage o***	***f usin***
t-5											**Anothe**	***r adva***	***tage o***	***f usin***	***g mult***
t-6										**Anothe**	***r adva***	***tage o***	***f usin***	***g mult***	***iple e***
t-7									**Anothe**	***r adva***	***tage o***	***f usin***	***g mult***	***iple e***	***xposur***
t-8								**Anothe**	***r adva***	***tage o***	***f usin***	***g mult***	***iple e***	***xposur***	***es of***
t-9							*Anothe*	***r adva***	***tage o***	***f usin***	***g mult***	***iple e***	***xposur***	***es of***	***a micr***
t-10						*Anothe*	***r adva***	***tage o***	***f usin***	***g mult***	***iple e***	***xposur***	***es of***	***a micr***	***o-mirr***
t-11					*Anothe*	***r adva***	***tage o***	***f usin***	***g mult***	***iple e***	***xposur***	***es of***	***a micr***	***o-mirr***	***or arr***
t-12				*Anothe*	***r adva***	***tage o***	***f usin***	***g mult***	***iple e***	***xposur***	***es of***	***a micr***	***o-mirr***	***or arr***	***ay is***
t-13			**Anothe**	***r adva***	***tage o***	***f usin***	***g mult***	***iple e***	***xposur***	***es of***	***a micr***	***o-mirr***	***or arr***	***ay is***	***that I***
t--14		**Anothe**	***r adva***	***tage o***	***f usin***	***g mult***	***iple e***	***xposur***	***es of***	***a micr***	***o-mirr***	***or arr***	***ay is***	***that I***	***t affo***
t-15	*Anothe*	***r adva***	***tage o***	***f usin***	***g mult***	***iple e***	***xposur***	***es of***	***a micr***	***o-mirr***	***or arr***	***ay is***	***that I***	***t affo***	***rds th***
t-16	*r adva*	***tage o***	***f usin***	***g mult***	***iple e***	***xposur***	***es of***	***a micr***	***o-mirr***	***or arr***	***ay is***	***that I***	***t affo***	***rds th***	***e poss***

FIG. 7

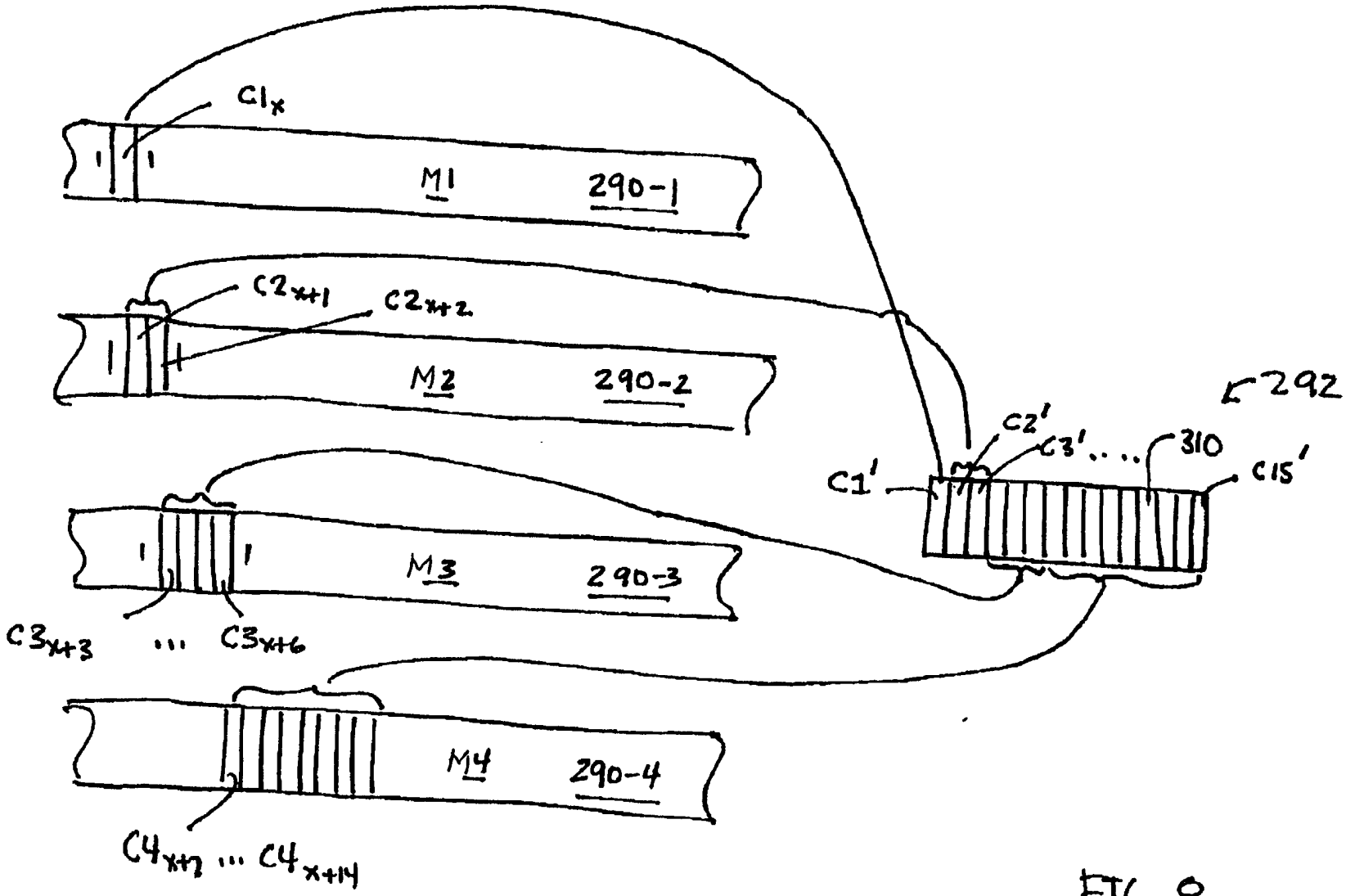

FIG. 8

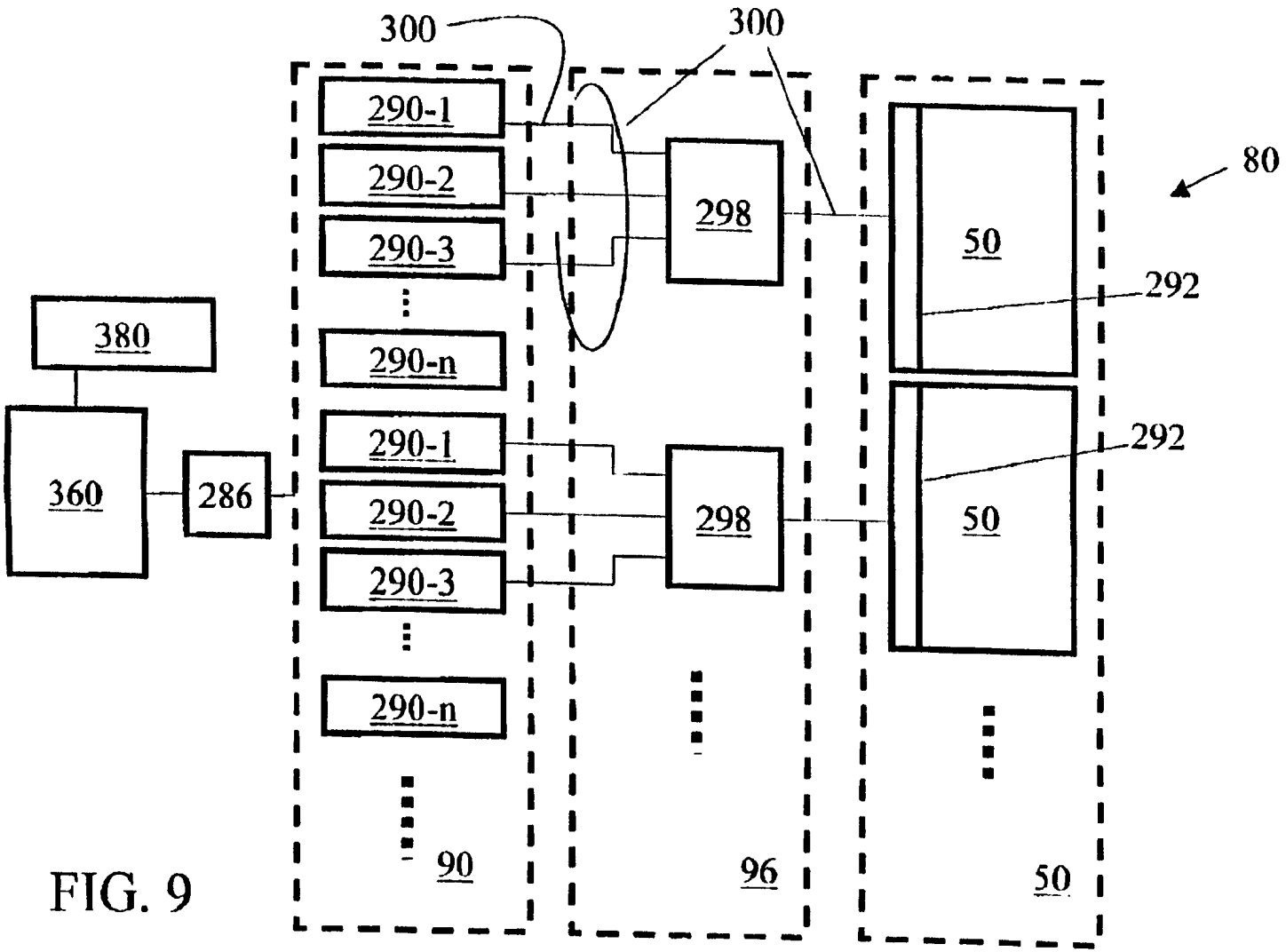

FIG. 9